



ESTD : 2005

AVANTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

An Autonomous Institution, Accredited by NAAC with 'A' Grade

(Approved by AICTE, Recognised by Govt. of Telangana & Affiliated to JNTU, Hyderabad)

Gunthapally (V), Abdullapurmet (M), R.R. Dist., Near Ramoji Filmcity, Hyderabad - 501 512.



EXAMINATION BRANCH

I YEAR M.TECH II SEMESTER – R25 MID-II EXAMINATIONS JULY-2026

TIMETABLE

TIME → FN: 10.00 AM TO 12.00 PM
TIME → AN: 02.00 PM TO 04.00 PM

BRANCH	13-07-2026(FN) MONDAY	14-07-2026(FN) TUESDAY	15-07-2026(FN) WEDNESDAY	16-07-2026(FN) THURSDAY
CSE (58)	Advanced Algorithms	Advanced Computer Architecture	Enterprise Cloud Concepts	Robotic Process Automation
VLSI (57)	Mixed Signal IC Design	FPGA Based System Design	VLSI Test and Testability	Low Power VLSI Design
EPS (07)	Digital Protection of Power System	Power System Dynamics	EHVAC Transmission	Electrical Vehicle Charging Techniques

DATE: 30-06-2026

CONTROLLER OF EXAMINATIONS
Controller of Examinations
AVANTHI INSTITUTE OF
ENGINEERING & TECHNOLOGY
Autonomous

G. S. Reddy

PRINCIPAL
AVANTHI INSTITUTE OF
ENGINEERING & TECHNOLOGY
Autonomous

G. S. Reddy

NOTE:
i) ANY OMISSIONS OR CLASHES IN THIS TIMETABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.
ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

Gunthapally (V), Abdullapurmet (M), R.R. Dist., Near Ramoji Filmcity, Hyderabad - 501 512.